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Engineers Successfully Port CERN's Analytics and Event Building Application at Line Rate with 100 ns Latency RapidIO Interconnect Hardware Platform

SAN JOSE, Calif., June 22, 2016 /PRNewswire/ -- Integrated Device Technology, Inc. (IDT ^[2]®) (NASDAQ: IDTI) announced today that CERN's data-processing framework has successfully been ported to a RapidIO[®]-enabled low-latency network that features multiple x86 CPU nodes, a world first in the high-performance computing market. The development comes during the first phase of IDT's collaboration with CERN ^[3] (the European Organization for Nuclear Research), part of a multi-year project announced last year that is designed to speed and improve data analytics at CERN's Large Hadron Collider (LHC) and data center.

Link:

[Article on 12newsnow.com](#) ^[4]

Copy of the coverage:

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[2] http://www.idt.com/?utm_campaign=interface_connectivity&utm_source=press_release&utm_medium=press_release
22-16

[3] <http://home.cern/>

[4] <http://www.12newsnow.com/story/32285378/idt-and-cern-openlab-mark-milestone-for-data-acquisition-and-data-center-analytics-applications-used-for-large-hadron-collider>

[5] <http://openlab-archive-phases-iv-v.web.cern.ch/sites/openlab-archive-phases-iv-v.web.cern.ch/files/press-coverage/Y/M/IDT%20and%20CERN%20openlab%20Mark%20Milestone%20for%20Data%20Acquisition%20and%202012%20News.pdf>